Towards Efficient Python Interpreter for Tiered Memory Systems

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Memory Capacity Bottleneck







CXL offers practical memory expansion 🙂

- Load/Store access over PCIe
- Large capacity with lower cost



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CXL has higher access latency than local DRAM

• With additional 70~90ns (~2x)



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- Runtime level
 - Explicitly control fine-grained object-level offloading 🙂
 - o Intrusive 😣
 - Supports only C/C++, JVM-based Supports



Python Popularity



*IEEE Spectrum

Can we achieve both transparency and object level tracing in Python runtime?



C++

CPython



C++ C++ Smart Pointers		
Hotness bits		
L	Rsvd	
С	bject Addre	ess

CPython





CPython





Key observation: Use CPython reference counting <u>changes</u> to infer Python object accesses

• Infer from reference count changes

Challenge 2: How to obtain live objects with low overhead?

- CPython GC holds only container objects
- Global Interpreter Lock

Cascade Tracing



<u>Real</u> heatmap from OS-based profiling.

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• Infer from reference count changes

Challenge 2: How to obtain live objects with low overhead?

• Cascade tracing with confined length/depth

Challenge 3: How to handle native executions (external libs)?

Rely on existing OS-based solutions, or function call stack inferences







• In progress

Challenge 2: How to obtain live objects with low overhead?

• Completed

Challenge 3: How to handle native executions (external libs)?

• In progress